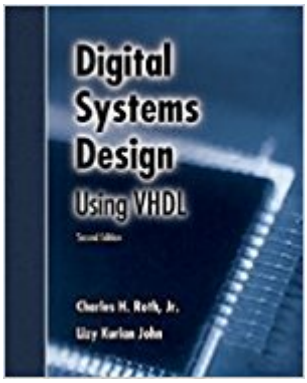
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Digital Systems Design Using VHDL



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Synopsis

Written for an advanced-level course in digital systems design, DIGITAL SYSTEMS DESIGN USING VHDL integrates the use of the industry-standard hardware description language VHDL into the digital design process. Following a review of basic concepts of logic design, the author introduces the basics of VHDL, and then incorporates more coverage of advanced VHDL topics. Rather than simply teach VHDL as a programming language, this book emphasizes the practical use of VHDL in the digital design process.

Book Information

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Customer Reviews

Charles Roth is Professor Emeritus in Electrical and Computer Engineering at the University of Texas at Austin, where he taught Digital Design for more than four decades. In addition to co-authoring DIGITAL SYSTEMS DESIGN USING VHDL, Dr. Roth has authored the successful FUNDAMENTALS OF LOGIC DESIGN and co-authored DIGITAL SYSTEMS DESIGN USING VERILOG.Lizy John is the B. N. Gafford Professor in Electrical and Computer Engineering at the University of Texas at Austin. Dr. John has been teaching and conducting research in computer architecture and digital systems design for almost two decades. She has coauthored DIGITAL SYSTEMS DESIGN USING VHDL and DIGITAL SYSTEMS DESIGN USING VERILOG and has edited several successful books on computer performance evaluation and workload characterization. She is an IEEE Fellow.

This book was required for a class and ended up being a great resource for learning VHDL. I only rented it because of the price and was disappointed to have to send it back.

Great book for learning VHDL and to use for reference. Will definitely keep in my library. I don’t think this is the best material for advanced users as some of my assignments require me to look elsewhere.

Book was delayed by weather but came brand new in package lick description

A lot of "examples" in this text need to be updated, as the coding in more current IDEs don’t use that specific terminology anymore. As for general understanding, it’s alright, clear and lots of examples, but as before, if you’re using a more current IDE software (like Xilinx or similar) a lot of the coding is incorrect and doesn’t translate to the program, but the structure of the coding does. Just be careful if you’re trying to implement what you’re learning on your own.

This was a book required by a VHDL class I took while completing my undergrad degree. I only ever opened it to look at the homework questions. I never had to read it because I had a good professor, but when skimming through the chapters, it didn’t seem like any of the ideas/concepts were presented in a way as clear as the professor made them.

Quite honestly, a PDF of this book with quick search would have been of better use to modern EE and ECE students.

Well I the book is not in an awful condition. But I was looking for books that were in really good condition and this has some pages that have a lot of things written down. And the CD is missing which apparently has a lot of useful information

I bought this book and it was recommended as a textbook for the VHDL course. I have this book and I am 100% sure that this book is NOT one of the good VHDL books you need/want in your library. It belongs to the "OK" level.chapter one:is the most valuable chapter. it contains an excellent review for every thing in digital logic, and guess what?chapter two:is a poor organized chapter that presents VHDL basics. after reading this chapter you will stop reading because you will start searching for other well organized books! that was my situation and 9 of my friends got similar

situation.after reading 3 books in VHDL, I highly recommend:1- only the first chapter of this book2- Circuit Design with VHDL by Pedroni3- some examples from "Design Recipes for FPGAs" by Wilson

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